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MEMC 00-1400(2806.1)
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Robert J. Falster et al.

Art Unit 1765

Serial No. 10/039,196

Filed January 2, 2002

Confirmation No. 7221

For PROCESS FOR PREPARING SINGLE CRYSTAL SILICON HAVING
IMPROVED GATE OXIDE INTEGRITY

March 6, 2002

Office of Initial Patent Examination's
Customer Service Center
Assistant Commissioner for Patents
Washington, D.C. 20231

SUPPLEMENTAL APPLICATION DATA SHEET

SIR:

Applicants listed some incorrect inventor information on the Application Data Sheet filed on January 2, 2002 respecting the above-referenced application. There was also a misspelling on the filing receipt received. In accordance with 37 CFR 1.76(d)(3) & (4), Applicants respectfully submit herewith a Supplemental Application Data Sheet. Applicants also respectfully request a corrected filing receipt reflecting this correction.

Respectfully submitted,



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SUPPLEMENTAL APPLICATION DATA SHEET

Application Information

<u>Serial No.::</u>	<u>10/039,196</u>
<u>Filing Date::</u>	<u>January 2, 2002</u>
Application Type::	Non-Provisional
Subject Matter::	Utility
Title::	Process For Preparing Single Crystal Silicon Having Improved Gate Oxide Integrity
Attorney Docket Number::	MEMC 00-1400(2806.1)
Request for Early Publication::	No
Request for Non-Publication::	No
Total Drawing Sheets::	17
Small Entity::	No
Petition Included::	No
Secrecy Order in Parent::	No

Applicant Information

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Correspondence Information

Correspondence Customer Number:: 000321

Representative Information

Representative Customer Number:: 000321

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	Non-Provisional of	60/259,362	01/02/02

Assignee Information

Assignee Name:: **MEMC Electronic Materials, Inc.**